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Information Disclosure Statement By Applicant			LAM2P255D1	Unassigned
(Use Several Sheets if Necessary)			Applicant: M. Saldana	
			Filing Date: February 26, 2004	Group: Unassigned

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class
CK	A	5,623,318	04/1997	Lee	348	614
CK	B	5,916,012	06/1999	Pant et al.	451	41
CK	C	6,139,400	10/2000	Sato et al.	451	10
CK	D	6,140,953	10/2000	Fukuyama	341	144
CK	E	6,422,918	07/2002	Avanzino et al.	451	41
	F					
	G					
	H					
	I					
	J					
	K					

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
CK	L	GB 2 162 977 A	12/2/86	EPO	H03M	7/00		X
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
CK	O	F.S. Lai, "The Architecture and Analysis of a Hybrid Number System Processor", 1992 IEEE, IBM Thomas J. Watson Research Ctr., Yorktown Heights, NY.
CK	P	Samueli, H., "An Improved Search Algorithm for the Design of Multiplierless FIR Filters with Powers-of-Two Coefficients", pp. 1044-1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1989.
CK	Q	Avizienis, "Signed-Digit Number Representations for Fast Parallel Arithmetic", pp. 389-400, IRE Transactions on Electronic Computers, Sept. 1961.

Examiner	<i>Chih-Kang</i>	Date Considered	<i>2/1/05</i>
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
 Include copy of this form with next communication to applicant.